IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Pe Patent Application of:

Leonard Forbes

Title:

PROGRAMMABLE MEMORY ADDRESS AND DECODE CIRCUITS WITH LOW TUNNE

BARRIER INTERPOLY INSULATORS

Attorney Docket No.: 1303.029US1

PATENT APPLICATION TRANSMITTAL

BOX PATENT APPLICATION

Commissioner for Patents Washington, D.C. 20231

We are transmitting herewith the following attached items and information (as indicated with an "X"):

 $\underline{\mathbf{X}}$ Return postcard.

Utility Patent Application under 37 CFR § 1.53(b) comprising:

Specification (66 pgs, including claims numbered 1 through 98 and a 1 page Abstract).

 $\underline{\mathbf{X}}$ Formal Drawing(s) (<u>17</u> sheets).

Signed Declaration (<u>3 pgs</u>). $\underline{\mathbf{X}}$

Signed Power of Attorney (1 pg.)

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Assignment of the invention to Micron Technology, Inc. (2 pgs) and Recordation Form Cover Sheet.

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